Inventors: Donald B. Eidson, et al. Application No. 09/887,877

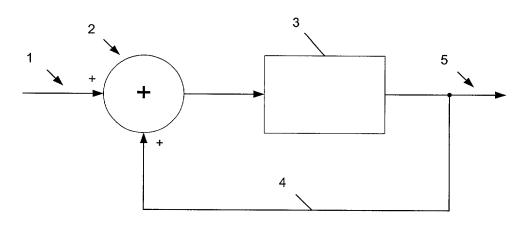
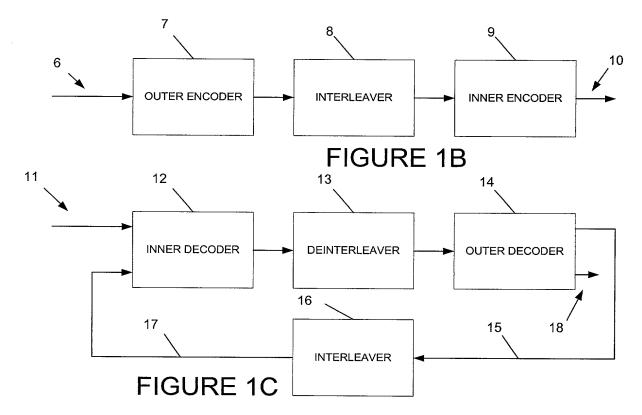


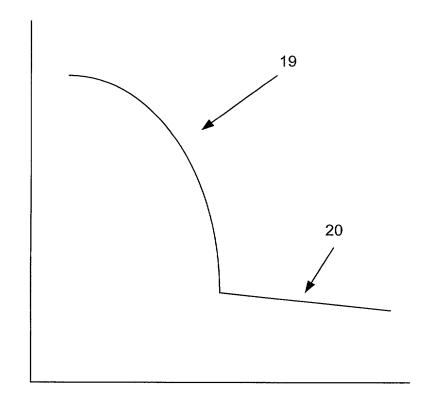
FIGURE 1A



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Dentrary Indent



 E_b/N_0

FIGURE 2

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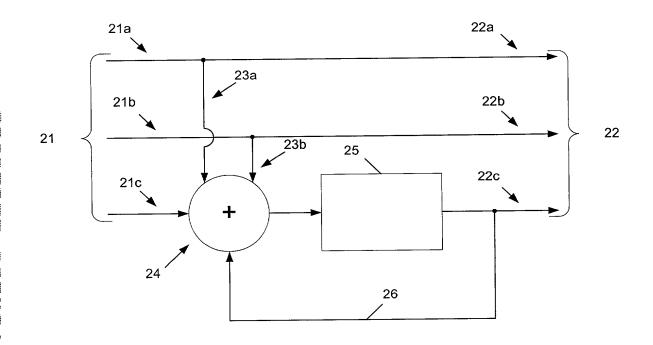


FIGURE 3A

Inventors: Donald B. Eidson, et al. Application No. 09/887,877

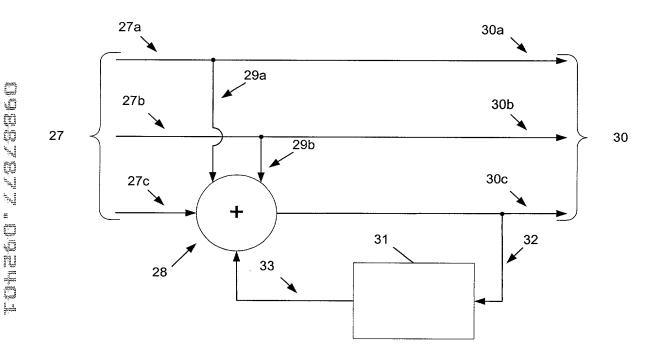


FIGURE 3B

Inventors: Donald B. Eidson, et al. Application No. 09/887,877

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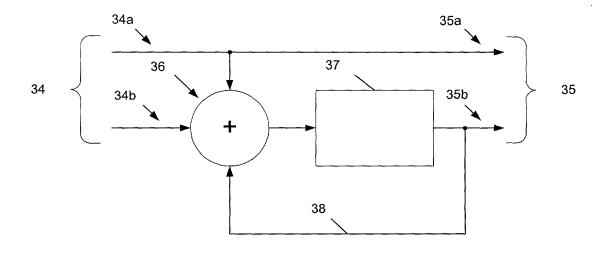
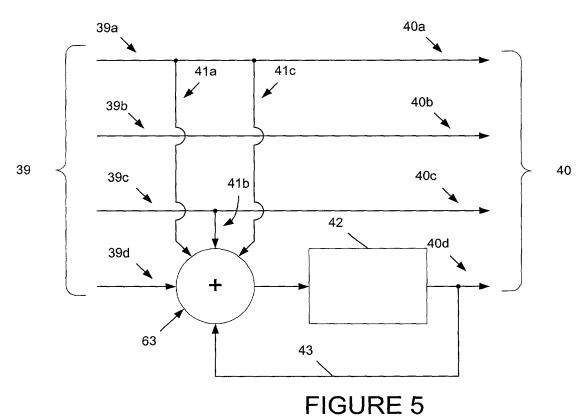


FIGURE 4



Inventors: Donald B. Eidson, et al. Application No. 09/887,877

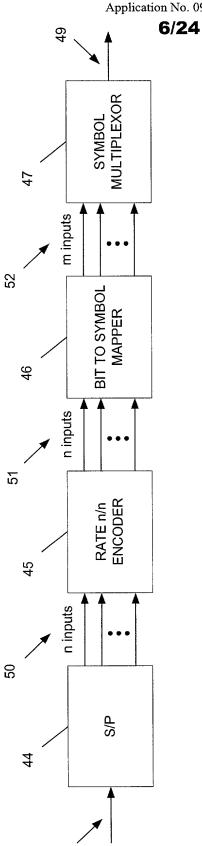


FIGURE 6

Inventors: Donald B. Eidson, et al. Application No. 09/887,877

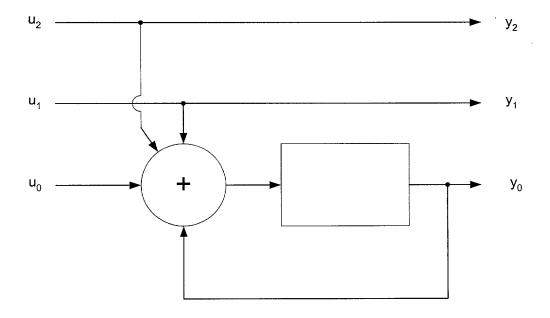
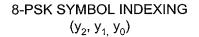


FIGURE 7A



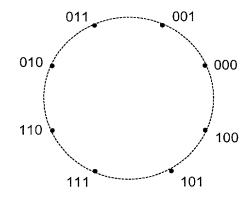
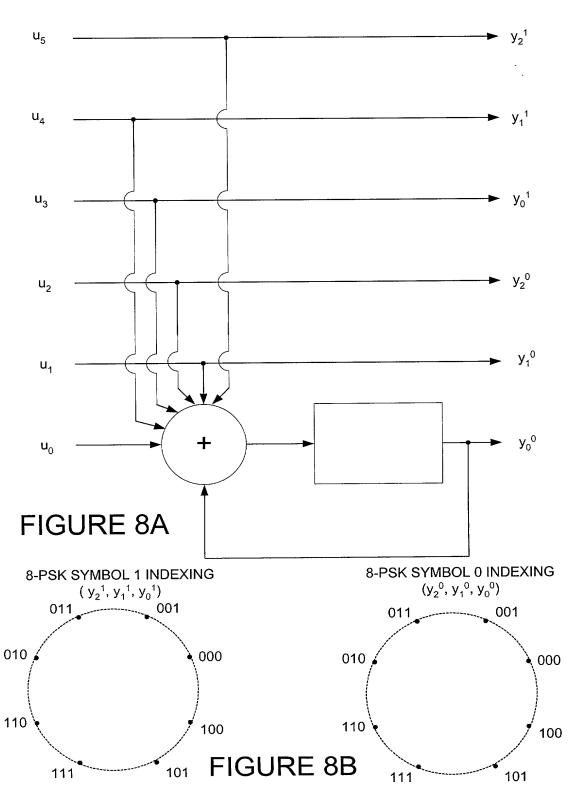


FIGURE 7B

Inventors: Donald B. Eidson, et al. Application No. 09/887,877



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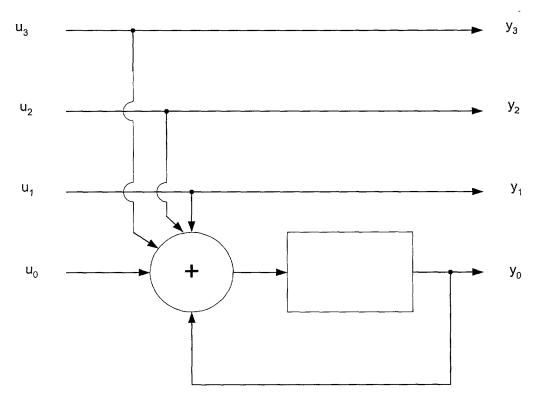


FIGURE 9A

16-QAM symbol indexing (y_3, y_2, y_1, y_0)

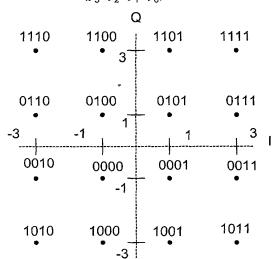


FIGURE 9B

Inventors: Donald B. Eidson, et al. Application No. 09/887,877

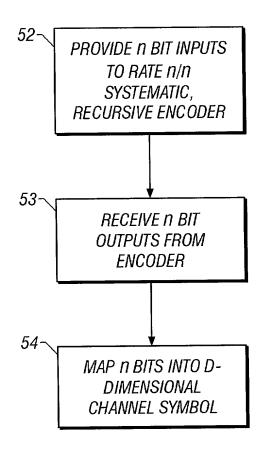
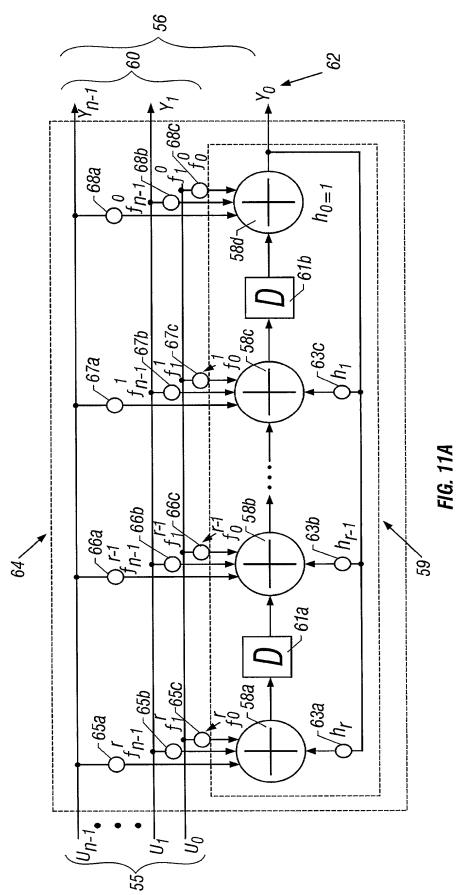
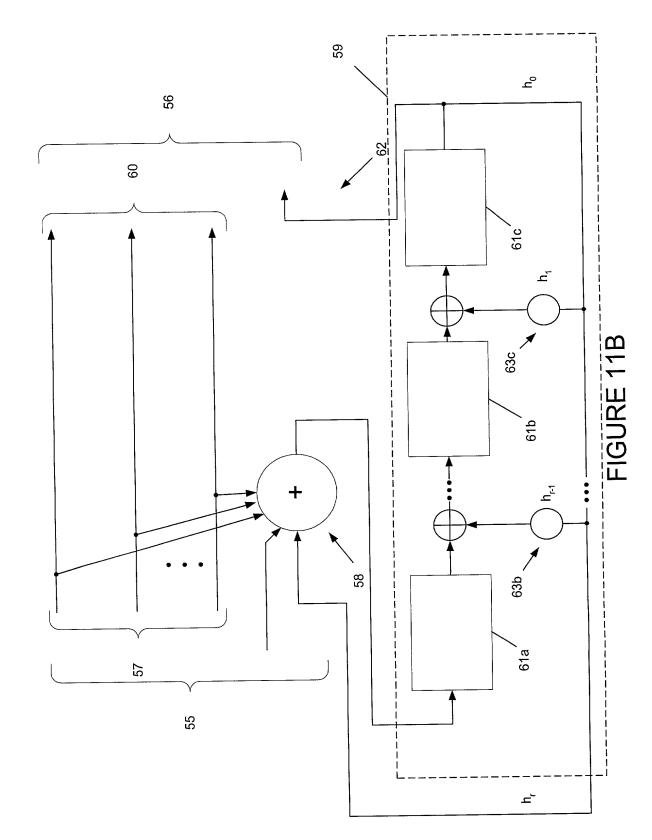


FIG. 10

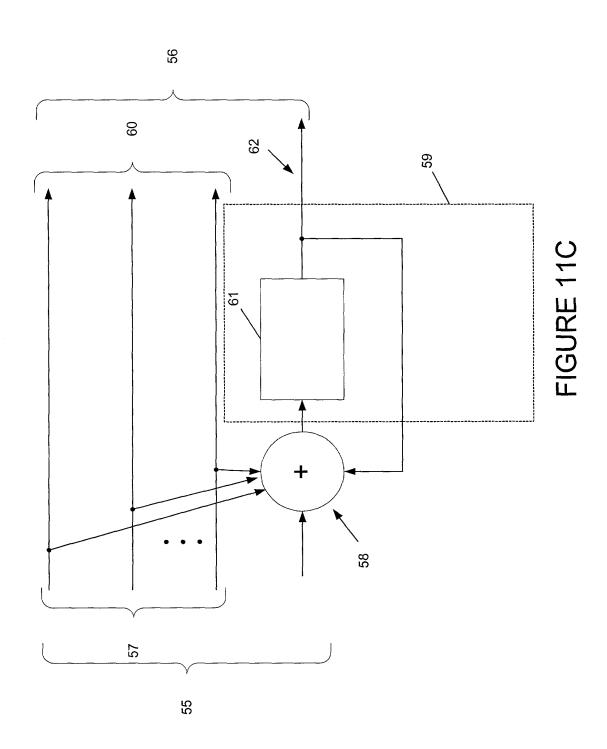
Inventors: Donald B. Eidson, et al. Application No. 09/887,877



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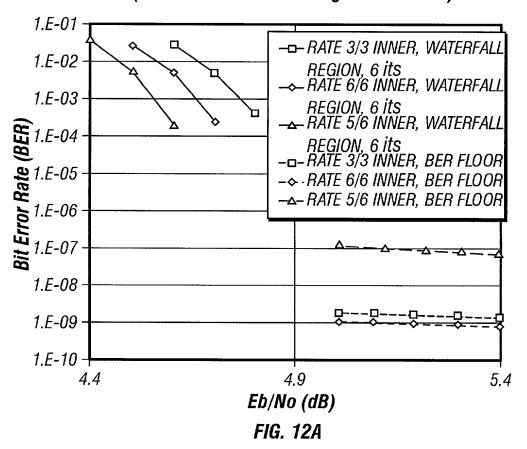


Inventors: Donald B. Eidson, et al. Application No. 09/887,877



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Rate 3/4 overall 8-PSK SCTCM performance: comparison of rate 3/3, 6/6, and 5/6 inner codes 15147-bit source data payload per block (BER floor estimated using Union Bound)



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Rate 5/6 overall 8-PSK SCTCM performance: rate 3/3 inner code vs rate 6/6 inner code 15125-bit source data payload per block (BER floor estimated using Union Bound)

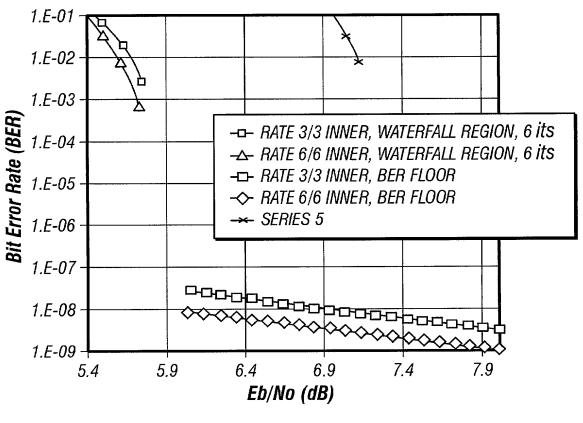
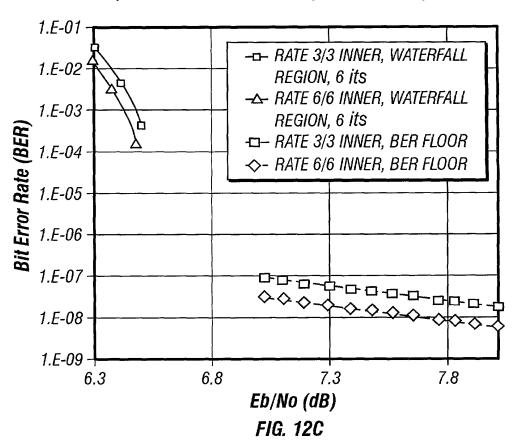


FIG. 12B

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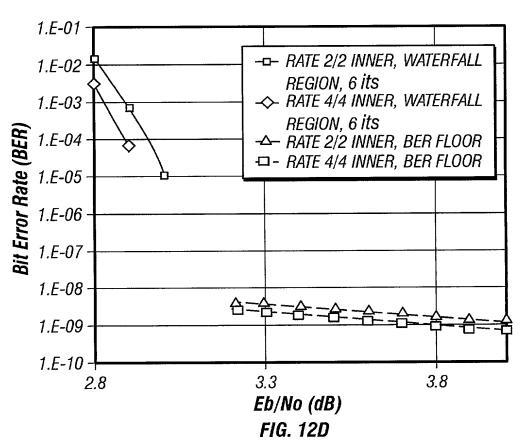
Rate 8/9 overall 8-PSK SCTCM performance: rate 3/3 inner code vs rate 6/6 inner code 15128-bit source data payload per block (BER floor estimated using Union Bound)



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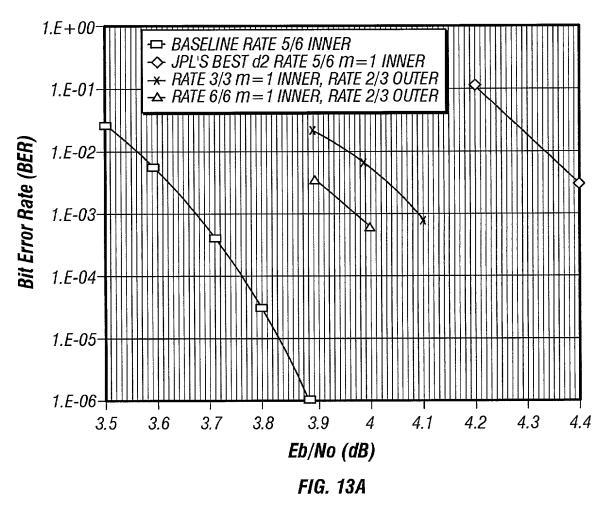
Rate 4/5 overall QPSK SCCC performance: rate 2/2 inner code vs rate 4/4 inner code 16384-bit source data payload per block (BER floor estimated using Union Bound)



Inventors: Donald B. Eidson, et al. Application No. 09/887,877

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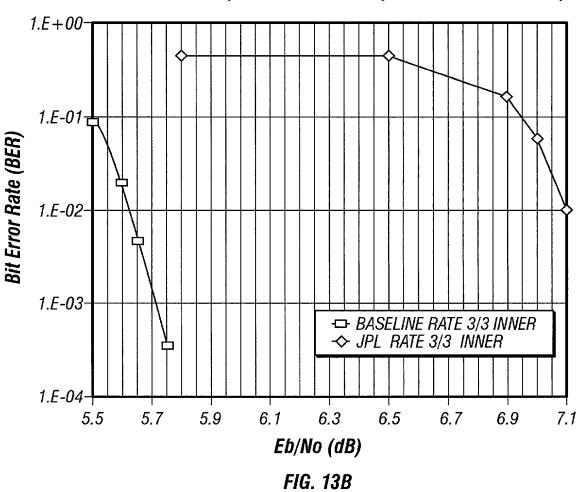
Simulated (Waterfall) Performance Rate 2/3 Overall, 15144 source bits, 5 iterations



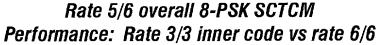
Inventors: Donald B. Eidson, et al. Application No. 09/887,877

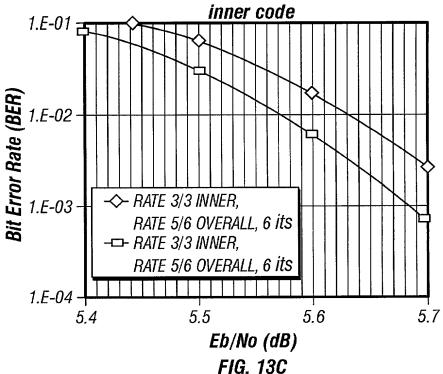
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Simulated (Waterfall) Performance Rate 5/6 Overall, (15147 source bits per block, 6 iterations)

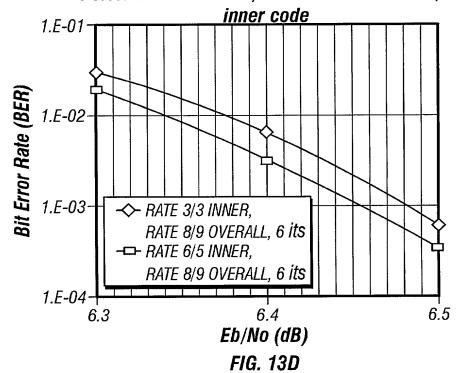


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Rate 5/6 overall 8-PSK SCTCM
Performance: Rate 3/3 inner code vs rate 6/6



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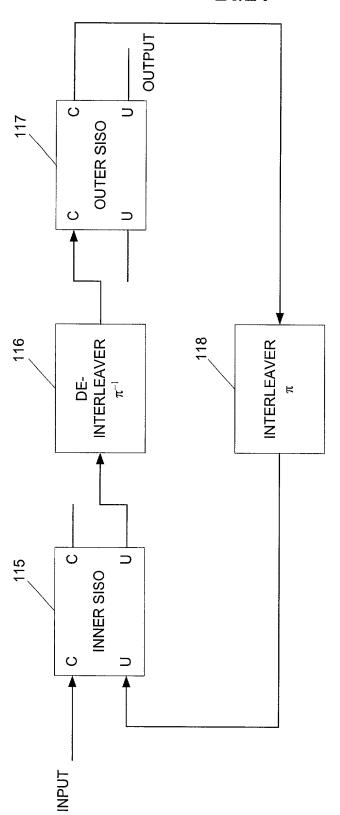


FIGURE 14

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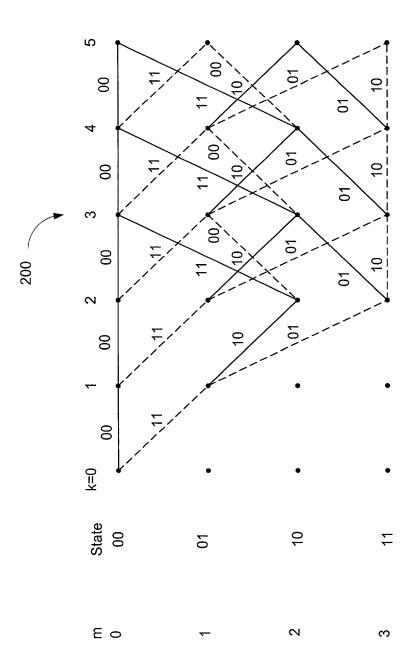


FIGURE 15

Inventors: Donald B. Eidson, et al. Application No. 09/887,877

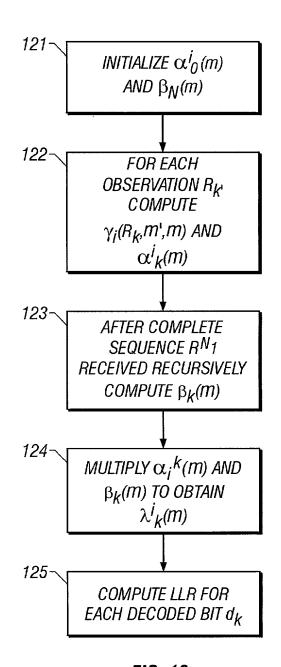


FIG. 16

Inventors: Donald B. Eidson, et al. Application No. 09/887,877

